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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/772,053	02/03/2004	Guobiao Zhang	GB8I	3940

7590 10/19/2005  
Guobiao Zhang  
P.O. Box 6182  
Stateline, NV 89449-6182

EXAMINER

PHAM, LONG

ART UNIT	PAPER NUMBER
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2814

DATE MAILED: 10/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/772,053

Applicant(s)

ZHANG, GUOBIAO

Examiner

Long Pham

Art Unit

2814

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) 13-17 is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-20 is/are allowed.
- 6) ☒ Claim(s) 18-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 03 February 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☒ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 09/26/05.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_.

**DETAILED ACTION**

***Election/Restrictions***

Applicant's election without traverse of claims 1-12 and 18-20 in the reply filed on 08/01/05 is acknowledged.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 18, 19, and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Moore (US patent 6,839,873) in combination with the applicant's admitted prior art (AAPA) of this application.

With respect to claim 18, Moore teaches a an integrated circuit IC chip comprising a mask programmable read-only-memory that stores test data.

Moore teaches using mask programmable read-only-memory for storing test data but fails to teach using non-electrically-programmable three-dimensional memory for storing test data.

AAPA teaches that non-electrically-programmable three-dimensional memory has the benefit of less sensitive to defect and consuming less power. See pages 1 and 2 of this application.

It would have been obvious to one of ordinary skill in the art of making semiconductor devices to use non-electrically-programmable three-dimensional memory for storing test data to achieve the above benefit.

Further with respect to claim 18, Moore in combination with AAPA teach using IC having non-electrically-programmable three-dimensional memory for storing test data but fail to teach using IC having non-electrically-programmable three-dimensional memory for storing test data of a printed-circuit-board-under-test.

It would have been obvious to one of ordinary skill in the art of making semiconductor devices to use IC having non-electrically-programmable three-dimensional memory to store test data of a printed-circuit-board-under-test because non-electrically-programmable three-dimensional memory has the benefit of less sensitive to defect and consuming less power.

With respect to claim 19, Moore and AAPA fail to teach connecting a test interface to non-electrically-programmable three-dimensional memory.

However, the connection of test interface to memory device is well-known.

With respect to claim 20, Moore in combination with AAPA teach using IC having non-electrically-programmable three-dimensional memory for storing test data but fail to teach using IC having non-electrically-programmable three-dimensional memory for storing test data of another IC.

It would have been obvious to one of ordinary skill in the art of making semiconductor devices to use IC having non-electrically-programmable three-dimensional memory to store test data of another IC because non-electrically-programmable three-dimensional memory has the benefit of less sensitive to defect and consuming less power.

### ***Allowable Subject Matter***

Claims 1-12 are allowed.

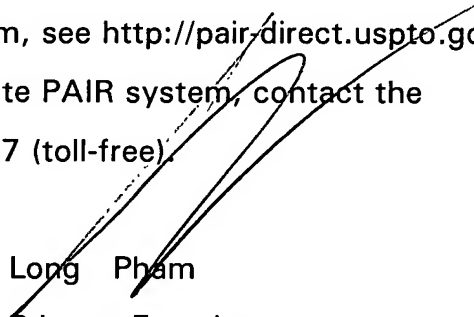
### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Long Pham whose telephone number is 571-272-1714. The examiner can normally be reached on Mon-Frid, 10am to 5pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on 571-272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Long Pham  
Primary Examiner  
Art Unit 2814

LP